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Advances in Computer Science, Engineering and Applications

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Preface

The Second International Conference on Computer Science, Engineering and Applications (ICCSEA-2012) was held in Delhi, India, during May 25–27, 2012. ICCSEA-2012 attracted many local and international delegates, presenting a balanced mixture of intellect from the East and from the West. The goal of this conference series is to bring together researchers and practitioners from academia and industry to focus on understanding computer science and information technology and to establish new collaborations in these areas. Authors are invited to contribute to the conference by submitting articles that illustrate research results, projects, survey work and industrial experiences describing significant advances in all areas of computer science and information technology.

The ICCSEA-2012 Committees rigorously invited submissions for many months from researchers, scientists, engineers, students and practitioners related to the relevant themes and tracks of the conference. This effort guaranteed submissions from an unparalleled number of internationally recognized top-level researchers. All the submissions underwent a strenuous peer-review process which comprised expert reviewers. These reviewers were selected from a talented pool of Technical Committee members and external reviewers on the basis of their expertise. The papers were then reviewed based on their contributions, technical content, originality and clarity. The entire process, which includes the submission, review and acceptance processes, was done electronically. All these efforts undertaken by the Organizing and Technical Committees led to an exciting, rich and a high quality technical conference program, which featured high-impact presentations for all attendees to enjoy, appreciate and expand their expertise in the latest developments in computer Science and Engineering research.

In closing, ICCSEA-2012 brought together researchers, scientists, engineers, students and practitioners to exchange and share their experiences, new ideas and research results in all aspects of the main workshop themes and tracks, and to discuss the practical challenges encountered and the solutions adopted. We would like to thank the General and Program Chairs, organization staff, the members of the Technical Program Committees and external reviewers for their excellent and tireless work. We sincerely wish that all attendees benefited scientifically from the conference and wish them every success in their research.

It is the humble wish of the conference organizers that the professional dialogue among the researchers, scientists, engineers, students and educators continues beyond the event and that the friendships and collaborations forged will linger and prosper for many years to come. We hope that you will benefit from the fine papers from the ICCSEA-2012 conference that are in this volume and will join us at the next ICCSEA conference.

David C. Wyld
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Contents

Networks and Communications

Partitioning and Internetworking Wireless Mesh Network with Wired Network for Delivery Maximization and QoS Provisioning	1
<i>Soma Pandey, Vijay Pande, Govind Kadambi, Stephen Bate</i>	
A New Secret Key Cipher: C128	15
<i>Indrajit Das, R. Saravanan</i>	
Optimal Bandwidth Allocation Technique in IEEE 802.11e Mobile Ad Hoc Networks (MANET)	25
<i>R. Mynuddin Sulthani, D. Sreenivasa Rao</i>	
Trusted AODV for Trustworthy Routing in MANET	37
<i>Sridhar Subramanian, Baskaran Ramachandran</i>	
Dynamic Fuzzy Based Reputation Model for the Assurance of Node Security in AODV for Mobile Ad-Hoc Network	47
<i>Arifa Azeez, K.G. Preetha</i>	
Policy Based Traffic in Video on Demand System	55
<i>Soumen Kanrar</i>	
Distance Aware Zone Routing Protocol for Less Delay Transmission and Efficient Bandwidth Utilization	63
<i>Dhanya Sudarsan, P.R. Mahalingam, G. Jisha</i>	
Mobile Data Offloading: Benefits, Issues, and Technological Solutions	73
<i>Vishal Gupta, Mukesh Kumar Rohil</i>	
Performance Analysis of Gigabit Ethernet Standard for Various Physical Media Using Triple Speed Ethernet IP Core on FPGA	81
<i>V.R. Gad, R.S. Gad, G.M. Naik</i>	

Assortment of Information from Mobile Phone Subscribers Using Chronological Model [IGCM]: Application and Management Perspective	91
<i>Neeraj Kumar, Raees A. Khan</i>	
Modeling Soft Handoffs' Performance in a Realistic CDMA Network	103
<i>Moses E. Ekpenyong, Enobong Umana</i>	
A Security Approach for Mobile Agent Based Crawler	119
<i>Vimal Upadhyay, Jai Balwan, Gori Shankar, Amritpal</i>	
Prospects and Limitations of Organic Thin Film Transistors (OTFTs)	125
<i>B.K. Kaushik, Brijesh Kumar, Y.S. Negi, Poornima Mittal</i>	
Active Learning with Bagging for NLP Tasks	141
<i>Ruy Luiz Milidíu, Daniel Schwabe, Eduardo Motta</i>	
Mining Queries for Constructing Materialized Views in a Data Warehouse	149
<i>T.V. Vijay Kumar, Archana Singh, Gaurav Dubey</i>	
Similarity Based Cluster Analysis on Engineering Materials Data Sets	161
<i>Doreswamy, K.S. Hemanth</i>	
A Chaotic Encryption Algorithm: Robustness against Brute-Force Attack	169
<i>Mina Mishra, V.H. Mankar</i>	
Introducing Session Relevancy Inspection in Web Page	181
<i>Sutirtha Kumar Guha, Anirban Kundu, Rana Dattagupta</i>	
Way Directing Node Routing Protocol for Mobile Ad Hoc Networks	193
<i>M. Neelakantappa, A. Damodaram, B. Satyanarayana</i>	
Web-Page Prediction for Domain Specific Web-Search Using Boolean Bit Mask	211
<i>Sukanta Sinha, Rana Dattagupta, Debajyoti Mukhopadhyay</i>	
Security Enhanced Digital Image Steganography Based on Successive Arnold Transformation	221
<i>Minati Mishra, Sunit Kumar, Subhadra Mishra</i>	
Impact of Bandwidth on Multiple Connections in AODV Routing Protocol for Mobile Ad-Hoc Network	231
<i>K.G. Preetha, A. Unnikrishnan, K. Paulose Jacob</i>	
Conceptualizing an Adaptive Framework for Pervasive Computing Environment	241
<i>Akhil Mohan, Nitin Upadhyay</i>	

Dynamic DCF Backoff Algorithm(DDBA) for Enhancing TCP Performance in Wireless Ad Hoc Networks	257
<i>B. Nithya, C. Mala, B. Vijay Kumar, N.P. Gopalan</i>	
Hybrid Cluster Validation Techniques	267
<i>Satish Gajawada, Durga Toshniwal</i>	
Energy Efficient and Minimal Path Selection of Nodes to Cluster Head in Homogeneous Wireless Sensor Networks	275
<i>S. Taruna, Sheena Kohli, G.N. Purohit</i>	
Texel Identification Using K-Means Clustering Method	285
<i>S. Padmavathi, C. Rajalaxmi, K.P. Soman</i>	
Single and Multi Trusted Third Party: Comparison, Identification and Reduction of Malicious Conduct by Trusted Third Party in Secure Multiparty Computing Protocol	295
<i>Zulfa Shaikh, Poonam Garg</i>	
Ubiquitous Medical Learning Using Augmented Reality Based on Cognitive Information Theory	305
<i>Zahra Mohana Gebril, Imam Musa Abiodunde Tele, Mohammed A. Tahir, Behrang Parhizkar, Anand Ramachandran, Arash Habibi Lashkari</i>	
A Secured Transport System by Authenticating Vehicles and Drivers Using RFID	313
<i>C.K. Marigowda, J. Thriveni, Javid K. Karangi</i>	
Virtualization of Large-Scale Data Storage System to Achieve Dynamicity and Scalability in Grid Computing	323
<i>Ajay Kumar, Seema Bawa</i>	
Behavioral Profile Generation for 9/11 Terrorist Network Using Efficient Selection Strategies	333
<i>S. Karthika, A. Kiruthiga, S. Bose</i>	
A Quantitative Model of Operating System Security Evaluation	345
<i>Hammad Afzali, Hassan Mokhtari</i>	
Energy Management in Zone Routing Protocol (ZRP)	355
<i>Dilli Ravilla, Chandra Shekar Reddy Putta</i>	
A New Approach for Vertical Handoff in Wireless 4G Network	367
<i>Vijay Malviya, Praneet Saurabh, Bhupendra Verma</i>	
An Analysis on Critical Information Security Systems	377
<i>Sona Kaushik, Shalini Puri</i>	

Emergency Based Remote Collateral Tracking System Using Google's Android Mobile Platform	391
<i>Prabhu Dorairaj, Saranya Ramamoorthy, Ashok Kumar Ramalingam</i>	
Performance Analysis of (AIMM-I46) Addressing, Inter-mobility and Interoperability Management Architecture between IPv4 and IPv6 Networks	405
<i>Gnana Jayanthi Joseph, S. Albert Rabara</i>	
Wireless Mobile Networks	
Strong Neighborhood Based Stable Connected Dominating Sets for Mobile Ad Hoc Networks	415
<i>Natarajan Meghanathan, Michael Terrell</i>	
OERD - On Demand and Efficient Replication Dereplication	425
<i>Vardhan Manu, Gupta Paras, Kushwaha Dharmender Singh</i>	
A Mathematical Model for Performance Evaluation and Comparison of MAP Selection Schemes in n Layer HMIPv6 Networks	435
<i>Abhishek Majumder</i>	
Utilizing Genetic Algorithm in a Sink Driven, Energy Aware Routing Protocol for Wireless Sensor Networks	447
<i>HosnyM. Ibrahim, NagwaM. Omar, Ali H. Ahmed</i>	
Load Balancing with Reduced Unnecessary Handoff in Hierarchical Macro/Femto-cell WiMAX Networks	457
<i>Prasun Chowdhury, Anindita Kundu, Iti Saha Misra, Salil K. Sanyal</i>	
A Study on Transmission-Control Middleware on an Android Terminal in a WLAN Environment	469
<i>Hiromi Hirai, Kaori Miki, Saneyasu Yamaguchi, Masato Oguchi</i>	
A Study of Location-Based Data Replication Techniques and Location Services for MANETs	481
<i>C.B. Chandrakala, K.V. Prema, K.S. Hareesha</i>	
A Comparative Analysis of Modern Day Network Simulators	489
<i>Debajyoti Pal</i>	
Design and Implementation of a RFID Based Prototype SmArt LibRARY (SALARY) System Using Wireless Sensor Networks	499
<i>K.S. Kushal, H.K. Muttanna Kadal, S. Chetan, Shivaputra</i>	
Optimal Route Life Time Prediction of Dynamic Mobile Nodes in Manets	507
<i>Ajay Kumar, Shany Jophin, M.S. Sheethal, Priya Philip</i>	

Reachability Analysis of Mobility Models under Idealistic and Realistic Environments	519
<i>Chirag Kumar, C.K. Nagpal, Bharat Bhushan, Shailender Gupta</i>	
Chaotic Cipher Using Arnolds and Duffings Map	529
<i>Mina Mishra, V.H. Mankar</i>	
Effect of Sound Speed on Localization Algorithm for Underwater Sensor Networks	541
<i>Samedha S. Naik, Manisha J. Nene</i>	
An Analytical Model for Power Control B-MAC Protocol in WSN	551
<i>V. Ramchand, D.K. Lobiyal</i>	
Enterprise Mobility – A Future Transformation Strategy for Organizations	559
<i>Jitendra Maan</i>	
Allocation of Guard Channels for QoS in Hierarchical Cellular Network ...	569
<i>Kashish Parwani, G.N. Purohit</i>	
Application Development and Cost Analysis for Content Based Publish Subscribe Model in Mobile Environment	579
<i>Medha A. Shah, P.J. Kulkarni</i>	
Energy Aware AODV (EA-AODV) Using Variable Range Transmission	589
<i>Pinki Nayak, Rekha Agarwal, Seema Verma</i>	
Automatic Speech Recognizer Using Digital Signal Processor	599
<i>Raghavendra M. Shet, Raghunath S. Holambe</i>	
Pre Decision Based Handoff in Multi Network Environment	609
<i>Manoj Sharma, R.K. Khola</i>	
A Swarm Inspired Probabilistic Path Selection with Congestion Control in MANETs	617
<i>Subhankar Joardar, Vandana Bhattacharjee, Debasis Giri</i>	
A Hierarchical CPN Model for Mobility Analysis in Zone Based MANET	627
<i>Moitreyee Dasgupta, Sankhayan Chaudhury, Nabendu Chaki</i>	
Sensor Deployment for Mobile Object Tracking in Wireless Sensor Networks	637
<i>Yingchi Mao, Ting Yin</i>	
ELRM: A Generic Framework for Location Privacy in LBS	647
<i>Muhamed Ilyas, R. Vijayakumar</i>	

Energy Efficient Administrator Based Secure Routing in MANET	659
<i>Himadri Nath Saha, Debika Bhattacharyya, P.K. Banerjee</i>	
Effect of Mobility on Trust in Mobile Ad-Hoc Network	673
<i>Amit Kumar Raikwar</i>	
Communications Security and Information Assurance	
Securing Systems after Deployment	685
<i>David (DJ) Neal, Syed (Shawon) Rahman</i>	
On the Security of Two Certificateless Signature Schemes	695
<i>Young-Ran Lee</i>	
Information Security Using Chains Matrix Multiplication	703
<i>Ch. Rupa, P.S. Avadhani</i>	
Formal Security Verification of Secured ECC Based Signcryption Scheme	713
<i>Atanu Basu, Indranil Sengupta, Jamuna Kanta Sing</i>	
Universal Steganalysis Using Contourlet Transform	727
<i>V. Natarajan, R. Anitha</i>	
Algorithm for Clustering with Intrusion Detection Using Modified and Hashed K – Means Algorithms	737
<i>M. Varaprasad Rao, A. Damodaram, N.Ch. Bhatra Charyulu</i>	
Z Transform Based Digital Image Authentication Using Quantization Index Modulation (Z-DIAQIM)	745
<i>Nabin Ghoshal, Soumit Chowdhury, Jyotsna Kumar Mandal</i>	
Secret Data Hiding within Tolerance Level of Embedding in Quality Songs (DHTL)	753
<i>Uttam Kr. Mondal, J.K. Mandal</i>	
A Novel DFT Based Information Embedding for Color Image Authentication (DFTIECIA)	763
<i>J.K. Mandal, S.K. Ghosal</i>	
A Real Time Detection of Distributed Denial-of-Service Attacks Using Cumulative Sum Algorithm and Adaptive Neuro-Fuzzy Inference System	773
<i>R. Anitha, R. Karthik, V. Pravin, K. Thirugnanam</i>	
Encryption of Images Based on Genetic Algorithm – A New Approach	783
<i>Jalesh Kumar, S. Nirmala</i>	

Content Based Image Retrieval Using Normalization of Vector Approach to SVM	793
<i>Sumit Dhariwal, Sandeep Raghuwanshi, Shailendra Shrivastava</i>	
Modified Grøstl: An Efficient Hash Function	803
<i>Gurpreet Kaur, Vidyavati S. Nayak, Dhananjoy Dey, S.K. Pal</i>	
Iris Recognition Systems with Reduced Storage and High Accuracy Using Majority Voting and Haar Transform	813
<i>V. Anitha, R. Leela Velusamy</i>	
Recovery of Live Evidence from Internet Applications	823
<i>Ipsita Mohanty, R. Leela Velusamy</i>	
Face Detection Using HMM–SVM Method	835
<i>Nupur Rajput, Pranita Jain, Shailendra Shrivastava</i>	
High Capacity Lossless Semi-fragile Audio Watermarking in the Time Domain	843
<i>Sunita V. Dhavale, R.S. Deodhar, L.M. Patnaik</i>	
Key Management Protocol in WIMAX Revisited	853
<i>Noudjoud Kahya, Nacira Ghoualmi, Pascal Lafourcade</i>	
Image Authentication Technique Based on DCT (IATDCT)	863
<i>Nabin Ghosal, Anirban Goswami, Jyotsna Kumar Mondal, Dipankar Pal</i>	
Survey on a Co-operative Multi-agent Based Wireless Intrusion Detection Systems Using MIBs	873
<i>Ashvini Vyavhare, Varsharani Bhosale, Mrunal Sawant, Fazila Girkar</i>	
A Binary Vote Based Comparison of Simple Majority and Hierarchical Decision for Survivable Networks	883
<i>Charles A. Kamhoua, Kevin A. Kwiat, Joon S. Park</i>	
A Novel Way of Protecting the Shared Key by Using Secret Sharing and Embedding Using Pseudo Random Numbers	897
<i>P. Devaki, G. Raghavendra Rao</i>	
Security Assurance by Efficient Non-repudiation Requirements	905
<i>S.K. Pandey, K. Mustafa</i>	
Poor Quality Watermark Barcodes Image Enhancement	913
<i>Mohammed A. Atiea, Yousef B. Mahdy, Abdel-Rahman Hedar</i>	
Hiding Data in FLV Video File	919
<i>Mohammed A. Atiea, Yousef B. Mahdy, Abdel-Rahman Hedar</i>	
Taxonomy of Network Layer Attacks in Wireless Mesh Network	927
<i>K. Ganesh Reddy, P. Santhi Thilagam</i>	

Implementing Availability State Transition Model to Quantify Risk Factor	937
<i>Shalini Chandra, Raees Ahmad Khan</i>	
Performance Analysis of Fast DOA Estimation Using Wavelet Denoising over Rayleigh Fading Channel on MIMO System	953
<i>A.V. Meenakshi, R. Kayalvizhi, S. Asha</i>	
Grid Computing	
DAGITIZER – A Tool to Generate Directed Acyclic Graph through Randomizer to Model Scheduling in Grid Computing	969
<i>D.I. George Amalarethnam, P. Muthulakshmi</i>	
A New Fault Tolerant Routing Algorithm for Advance Irregular Alpha Multistage Interconnection Network	979
<i>Ved Prakash Bhardwaj, Nitin</i>	
Comparing and Analyzing the Energy Efficiency of Cloud Database and Parallel Database	989
<i>Jie Song, Tiantian Li, Xuebing Liu, Zhiliang Zhu</i>	
A Grid Fabrication of Traffic Maintenance System	999
<i>Avula Anitha, Rajeev Wankar, C. Raghavendra Rao</i>	
Intrusion Detection and QoS Security Architecture for Service Grid Computing Environment	1009
<i>Raghavendra Prabhu, Basappa B. Kodada, K.M. Shivakumar</i>	
Service Composition Design Pattern for Autonomic Computing Systems Using Association Rule Based Learning	1017
<i>Mohammed A.R. Quadri, Vishnuvardhan Mannava, T. Ramesh</i>	
An Enhancement to AODV Protocol for Efficient Routing in VANET – A Cluster Based Approach	1027
<i>M.C. Aswathy, C. Tripti</i>	
Human Emotion Recognition and Classification from Digital Colour Images Using Fuzzy and PCA Approach	1033
<i>Shikha Tayal, Sandip Vijay</i>	
A New Process Placement Algorithm in Multi-core Clusters Aimed to Reducing Network Interface Contention	1041
<i>Ghobad Zarrinchian, Mohsen Soryani, Morteza Analoui</i>	
Resource Based Optimized Decentralized Grid Scheduling Algorithm	1051
<i>Piyush Chauhan, Nitin</i>	

Web-Based GIS and Desktop Open Source GIS Software: An Emerging Innovative Approach for Water Resources Management	1061
<i>Sangeeta Verma, Ravindra Kumar Verma, Anju Singh, Neelima S. Naik</i>	
A Design Pattern for Service Injection and Composition of Web Services for Unstructured Peer-to-Peer Computing Systems with SOA	1075
<i>Vishnuvardhan Mannava, T. Ramesh, Mohammed A.R. Quadri</i>	
A Policy Driven Business Logic Change Management for Enterprise Web Services	1085
<i>M. Thirumaran, P. Dhavachelvan, G. Naga Venkata Kiran</i>	
Author Index	1095

Partitioning and Internetworking Wireless Mesh Network with Wired Network for Delivery Maximization and QoS Provisioning

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Abstract. Wireless mesh architecture is a first step towards providing high-bandwidth network coverage. This architecture has major drawback of losing the bandwidth over multiple hops thereby resulting in poor quality of service (QoS) at nodes separated by more than two hops. This paper proposes a three step approach to guarantee bandwidth demand at each node of the network thereby providing high quality of service even to nodes separated by large distances from each other. The authors have presented a novel method for clustering the nodes and load sharing amongst the clusters based on graph partitioning approach. This work also presents a system and method of integrating Wireless Mesh Networks (WMN) with wired network for further increase in the QoS.

Keywords: Partitioning, Internetworking, Wireless mesh network, IEEE 802.11s.

1 Introduction

The wireless mesh network (WMN) is an emerging technology to extend the use of wireless communication. Mesh architecture sustains signal strength by breaking long distances into a series of shorter hops. Intermediate nodes not only boost the signal, but cooperatively make the forwarding decisions based on their knowledge of the network. Such architecture provides high network coverage, spectral efficiency, and economic advantage. Throughout the paper we use the IEEE 802.11s standard for infrastructure mode WMN. The authors have chosen IEEE802.11s as the WMN because major part of this work focuses on providing a wired backup to mesh nodes, and this standard already has a protocol defined for internetworking between the 802.11 and non 802.11 networks. But this work can be generalized to optimize any infrastructure based wireless mesh network. For this reason this work does not differentiate between a Mesh Point (MP) and Mesh Access Point (MAP) as separate entities as both are sources of bandwidth demand. Therefore hereafter both these entities will be called nodes whereas the Mesh Portal Point (MPP) will be called the gateway node.

At Layer 2 of WMN the crucial QoS parameter that can be delivered is the bandwidth demand of a node. It is a well known fact that wireless networks yield low throughput and poor QoS because they are bandwidth starved due to radio spectrum limitations. The authors suggest that if bandwidth demand at a node can be met; QoS constraints can be satisfied.

In subsequent sections the authors use a graph model of WMN to present a novel method of QoS provisioning in WMN. This work provides a three step approach to satisfy the bandwidth demand of all the nodes in a WMN.

- **Step I:** In order to share the load amongst gateways there has to be well defined clusters around the gateways. In [5] the authors have already provided a partitioning mechanism to create well defined clusters around gateways. This paper moves further and maps these partitions on to the Adjacency matrix of the graph model of WMN
- **Step II:** Using the concept of '*Supergraphs*' authors proceed to share the load amongst the partitions dynamically. The load sharing algorithm ensures that the under loaded partitions share the load of their neighboring overloaded partitions under certain mathematically validated constraints. This algorithm also ensures that there is no need to re-compute the partitions every time a node transits to a neighboring partition for load sharing.
- **Step III:** In case the constraints defined for load sharing in step II are not satisfied and nodes are still bandwidth starved, then the authors provide the partitions with a wired network backup. This step defines the set of constraints to be observed while transiting a node to the wired network. Although the authors' choice of network is Broadband over Powerline (BPL) but this work is not limited to BPL and is applicable to any wired Internet Protocol (IP) network. For more on BPL-WMN inter-networking refer [12]

Note: Due to space limitations the authors have kept the contents of this paper limited to defining the mathematical constraints and presenting an algorithm for partitioning, load sharing and wired network interworking. In their coming paper authors have presented a detailed protocol between the gateways, core router and Dynamic Host Configuration Protocol (DHCP) server which is to be observed while implementing these algorithms. The authors have presented a centralized protocol to implement these algorithms

2 Motivation and Related Work

WMN suffer from the limitation of throughput drop and bandwidth loss over multiple hops, Li et al. [4]. Reducing the distance between nodes in the WMN loses the very purpose of mesh networking which is to provide wider coverage area with minimal infrastructure. Robinson et al.[2] and Aoun et al. [3] propose to increase throughput by introducing multiple gateways . Placement of multiple gateways throughout the mesh does not always result in more throughput as proved by [5]. In previous literature Xie et al.,[7] and Bejerano et al.,[8] have suggested to improve this shortcoming by creating clusters around each gateway and then make provisions for load sharing amongst these clusters. Nandiraju et al.,[6] and Bejerano et al., [8] have pointed out

that by clustering the nodes in to non overlapping clique increases the throughput of networks. But partitioning the graphs in itself is an NP hard problem. With every change in load or transition of nodes amongst partitions, there is need to re-compute the partitions. This situation gets worse when nodes move to another network rather than another partition as the nodes are no longer a part of the same WMN. This is called 'loss of wireless neighborhood' problem. Current literature addresses this by emphasizing on the spanning tree computation periodically, thereby identifying all the nodes belonging to the same network. Partitioning the network every time with a changing load and node scenario is a serious problem as the whole network remains non-operational throughout this computation thereby reducing the network throughput. In this paper, authors have made a novel attempt to represent the network graph model in its adjacency matrix form. The adjacency matrix representation of the graph model of WMN preserves the neighborhood of each and every node, irrespective of whether it moves to a neighboring partition or to another network. The adjacency matrix representation eliminates the need to recreate partitions, even when the WMN is interworked with another network. This method increases the network throughput because it creates the partitions only once during the network design phase. Once the partitions are created, they are simply mapped onto the adjacency matrix of the graph model of WMN, which is done by our graph partitioning algorithm. Thereafter there is no need to continuously partition the network with changing load as in [7] and [8].

Contributions of this paper are

- One time partitioning of WMN.
- System and method to map partitions of a WMN on to the adjacency matrix of its graph model.
- System and method to transit nodes amongst these partitions directly using the adjacency matrix.
- System and method to interwork the WMN with another wired network and mapping the same onto the global adjacency matrix.
- Defining a set of mathematical bounds and constraints for load sharing and node transitions amongst partitions.
- A locally recursive algorithm for node selection and transition to the BPL or any other wired network.
- Nodes continue to remain part of WMN with their neighborhood preserved in the adjacency matrix, irrespective of whether they transit to another partition or to another wired network.

3 Notations and Assumptions

Let undirected planar Graph $\mathcal{G} = (\mathcal{V}, \mathcal{E})$ represent a WMN 'W' with n number of nodes and gateways. The graph nodes and gateways represent the vertices and wireless links are represented by edges between the nodes. Self loops are not permitted. Let \mathcal{V} be the set of vertices v_1, v_2, \dots, v_n such that $|\mathcal{V}| = n$ and \mathcal{E} be the set of edges e_1, e_2, \dots, e_m such that $|\mathcal{E}| = m$. Total number of gateways/MPP is assumed to be k . Then, $\mathcal{G}_1, \mathcal{G}_2, \dots, \mathcal{G}_k$ will be the k distinct partitions of graph \mathcal{G} , each with one gateway.

Let P_i denote a node corresponding to the contracted subgraph \mathcal{G}_i in the supergraph of the partitioned \mathcal{G} . Let n_i be the number of vertices in \mathcal{G}_i . Let \mathcal{V}_i be vertex set for \mathcal{G}_i $\forall i = 1 \dots k$. Let $\mathcal{A}(\mathcal{G})$ be the Adjacency matrix corresponding to graph \mathcal{G} and $\mathcal{A}(\mathcal{G}_i)$ be the adjacency matrix for partition \mathcal{G}_i .

$\mathcal{B}(\mathcal{G})$: Incidence matrix corresponding to graph \mathcal{G}

$\mathcal{B}(\mathcal{G}_i)$: Incidence matrix corresponding to the partition i of graph \mathcal{G}

$\mathcal{C}(\mathcal{G})$: Cycle matrix corresponding to graph \mathcal{G}

$\mathcal{C}(\mathcal{G}_i)$: Cycle matrix corresponding to the partition i of graph \mathcal{G}

Let C_i be the capacity of i^{th} gateway in partition i . $Q\{\mathcal{A}(\mathcal{G}_i)\}$ denotes QoS available at partition \mathcal{G}_i

R_i : current bandwidth demand (load) of partition \mathcal{G}_i

U_i : Upper working demand limit of QoS for partition \mathcal{G}_i

L_i : Lower working demand limit of QoS for partition \mathcal{G}_i

Under normal load conditions if demand of node n_i is d_i then total load of partition \mathcal{G}_i with n_i number of nodes is

$$R_i = \sum_{j=1}^{n_i} d_j \quad \forall i = 1, \dots, k$$

Overload of a partition is given by

$$overload(\mathcal{G}_i) = \begin{cases} 0, & \text{if } R_i < C_i \\ R_i - C_i, & \text{Otherwise} \end{cases}$$

4 Step I: Selective Partitioning

Selective Partitioning is called so because a graph is partitioned with certain constraints. The constraint in our case is that each partition must have exactly one gateway. This algorithm assumes that initial partitioning of WMN is already done. A WMN can be partitioned using any of the graph partitioning procedures available in literature [10]. Alternatively researchers can also use the node marking and partitioning algorithms presented by authors in [5] and [9]. First we present an observation on \mathcal{A}

$\mathcal{A}(\mathcal{G})$ can be written in block diagonal form as

$$\mathcal{A}(\mathcal{G}) = \begin{bmatrix} [\mathcal{A}(\mathcal{G}_1)] & \dots & 0 \\ \vdots & [\mathcal{A}(\mathcal{G}_2)] & \vdots \\ 0 & \dots & [\mathcal{A}(\mathcal{G}_k)] \end{bmatrix}_{n \times n}$$

Based on this observation we present the Algorithm I for selective graph partition

1. From WMN create $\mathcal{G}(v, e)$ with one gateway
2. From $\mathcal{G}(v, e)$ construct $\mathcal{A}(\mathcal{G})$.
3. Now take $[\mathcal{A}(\mathcal{G})]_{n \times n}$ and identify the 1st gateway of the WMN represented by $\mathcal{G}(v, e)$

4. Around the first gateway create a partition $[\mathcal{A}_{11}]_{n_1 \times n_1}$ by relabeling / visiting nodes and demand augmentation of nodes in $[\mathcal{A}(\mathcal{G})]$ against gateway capacity C_1 . Refer authors' paper [9] for complete procedure on node marking and relabeling.
5. Now identify the second gateway in $\mathcal{A}_2 = [\mathcal{A}] - \begin{bmatrix} \mathcal{A}(\mathcal{G}_1) & 0 \\ 0 & \ddots \end{bmatrix}$ now create $[\mathcal{A}_{22}]_{n_2 \times n_2}$. Relabel $\mathcal{A}(\mathcal{G})$ such that all the nodes adjacent to the second gateway have their corresponding rows and columns next to the second gateway row and column thereby creating the second matrix $[\mathcal{A}_{22}]$.
6. Now get $\mathcal{A}_3 = [\mathcal{A}] - \begin{bmatrix} \mathcal{A}(\mathcal{G}_1) & \dots & 0 \\ \vdots & \mathcal{A}(\mathcal{G}_2) & \vdots \\ 0 & \dots & \ddots \end{bmatrix}$ and create $[\mathcal{A}_{33}]_{n_3 \times n_3}$ by relabeling / node visiting fundamental on $[\mathcal{A}_3]$
7. Repeat step 4 to 6 till the last partition $[\mathcal{A}_k]$ is formed such that $\sum_{i=1}^k n_i = n$, this leads to k disjoint initial partitions
8. Hence WMN in the initial partition looks like

$$\mathcal{A}(\mathcal{G}) = \begin{bmatrix} [\mathcal{A}(\mathcal{G}_1)] & \dots & & \\ \vdots & [\mathcal{A}(\mathcal{G}_2)] & & \vdots \\ & & \ddots & \\ & \dots & & [\mathcal{A}(\mathcal{G}_k)] \end{bmatrix}$$
 obtained by relabeling and node visiting. Thus the adjacency matrix has all the partitions defined by their own adjacency matrix right across the diagonal. The other elements of the adjacency matrix are as before corresponding to the various rows and columns as in the initial adjacency matrix. Thus initial set of disjoint partitions in \mathcal{A} matrix is created
9. End

Note: The partitioned adjacency matrix of step 8 has to be created only once

From Algorithm I we get the WMN partitioned in k partitions each having one gateway. We denote the i^{th} partition by subgraph \mathcal{G}_i and its adjacency matrix by $\mathcal{A}(\mathcal{G}_i)$. It can be seen that $\mathcal{V}_i \cap \mathcal{V}_j = \emptyset \forall i, j \in [1 \dots k] \exists i \neq j$. Hence any vertex in \mathcal{G}_i can be made as gateway. Each \mathcal{G}_i will have n_i number of nodes. All these partitioned matrices must satisfy the condition $\mathcal{B}(\mathcal{G}_i) \times [\mathcal{C}(\mathcal{G}_i)]^T = \mathcal{C}_i \times [\mathcal{B}(\mathcal{G}_i)]^T = 0 \pmod{2}$ where superscript T denotes the transposed matrix [11].

5 Step II QoS Provisioning by Load Sharing

Before moving to the load sharing algorithm we define a few terms and formulate some theorems.

Definition I: A 'Cut set' \mathcal{E}_{ij} is set of all the edges between two partitioned subgraphs \mathcal{G}_i and \mathcal{G}_j of \mathcal{G} such that for each edge both its incident vertices belong to two different partitions \mathcal{G}_i and \mathcal{G}_j

Definition II: A 'Supergraph' \mathcal{G}^2 of \mathcal{G} is the graph obtained such that each vertex P_i of \mathcal{G}^2 represents the partition subgraph \mathcal{G}_i and edge e_i is the edge belonging to the cut set \mathcal{E}_{ij} . Fig1

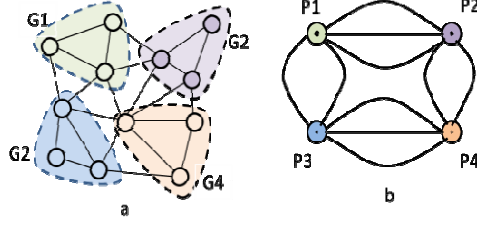


Fig. 1: **a:** A Graph G with its 4 partitions. **b:** Supergraph G^2 of G .

We present following set of properties of the Supergraphs

Property I: Since a WMN with k gateways will have k partitions therefore number of vertices in G^2 will be k .

Property II: G^2 will be a multigraph because there can be many edges for each pair of partition cut set.

Property III: G^2 can also be a complete graph of k vertices i.e. K_k graph. Consequently the following proof is needed.

Lemma 1: If G^2 is a super graph of G and if G is planar G^2 will also be planar

Proof: By contradiction let us assume that G^2 is non planar. Then G^2 will have intersecting edges. Now since G is contracted to G^2 . This implies that G also has intersecting edges. As a result G has to be non planar. Thus by contradiction since $G(v, e)$ is a planar therefore $G^2 \cong G(v, e)$ is also planar. **(Q.E.D)**

The implication of the above lemma is for the reinforcement of fact that any node transition from one partition to another does not contradict the planar structure of the graph. The transition of nodes can happen from one partition to another if the two partitions are neighbors. Partition/node at one hop distance are called neighbors.

In \mathcal{A} we retain only the partitions on the diagonal and replace remaining elements by 0. This matrix we call as \mathcal{A}' . Then,

$$\Rightarrow \mathcal{A}'(G) = \begin{bmatrix} \overbrace{[\mathcal{A}(G_1)]}^{n_1} & \overbrace{[\mathcal{A}(G_2)]}^{n_2} & \cdots & \overbrace{[\mathcal{A}(G_k)]}^{n_k} \\ \vdots & \vdots & \ddots & \vdots \end{bmatrix}$$

Here n_i is number of vertices in partition subgraph G_i . Let, $\mathcal{A}''(G) = \mathcal{A}(G) - \mathcal{A}'(G)$. Then,

$$\mathcal{A}''(G) = \mathcal{A}(G) - \mathcal{A}'(G) = \begin{bmatrix} [0] & [\mathcal{A}]_{n_1 \times n_2} & [\mathcal{A}]_{n_1 \times n_3} & \cdots & [\mathcal{A}]_{n_1 \times n_k} \\ [\mathcal{A}]_{n_1 \times n_2}^T & [0] & & & \vdots \\ \vdots & & \ddots & & \\ [\mathcal{A}]_{n_1 \times n_k}^T & \cdots & & [\mathcal{A}]_{n_{k-1} \times n_k} & [0] \end{bmatrix}$$

Proposition 1: \mathcal{P}_i has 'q' paths to \mathcal{P}_j iff there are q number of non zero entries in $[\mathcal{A}]_{n_i \times n_j}$ of matrix $\mathcal{A}''(\mathcal{G})$

Proof: The number of non zero entries in $[\mathcal{A}]_{n_i \times n_j}$ is the edge cut set of \mathcal{P}_i , and \mathcal{P}_j . Now without loss in generality, \mathcal{P}_i can be termed as node, $\forall i = (1 \dots k)$ and if \mathcal{P}_i is neighbor of \mathcal{P}_j then it can be joined by edges from their edge cut set.

If $\mathcal{A}(\mathcal{G}_i)$ is operating at its limit U_i then node need to be transited to $\mathcal{A}(\mathcal{G}_j)$ operating at limit L_j . In the next proposition we define the operation in order to balance the load on the \mathcal{G}^2 graph.

Proposition 2: For any partition \mathcal{P}_i if the cumulative QoS requirements are not met then following operations can be performed

Transit the node to the neighboring partition.

Create a partition in the sub partition

Proof: Consider the k partitions as created earlier. These partitions operating under normal load must satisfy following condition

$$[Q(\mathcal{A}(\mathcal{G}_i))|_{n_i} \geq Q(\mathcal{A}(\mathcal{G}_i))|_{n_i} \geq [Q(\mathcal{A}(\mathcal{G}_i))] \quad \forall i \in [1 \dots k]$$

This is same as $U_i \geq R_i \geq L_i$ (normal load constraint)

To satisfy the QoS requirement if a node is shifted to powerline network, then it is same as further partitioning of partition $\mathcal{A}(\mathcal{G}_i)$. Let us assume that $\mathcal{A}(\mathcal{G}_i)^j$ is the j^{th} partition of \mathcal{G}_i . Likewise, if there are ω sub partitions of $\mathcal{A}(\mathcal{G}_i)$ then the following condition holds good

$$Q(\mathcal{A}(\mathcal{G}_i)) = \sum_{j=1}^{\omega} Q(\mathcal{A}(\mathcal{G}_i)^j)$$

where

$$\sum_{i=1}^k n_i = n = \text{total number of nodes}$$

Now if $Q(\mathcal{A}(\mathcal{G}_i))|_{n_i} \geq [Q(\mathcal{A}(\mathcal{G}_i))|_{n_i}]$ then following can be carried out

1. Transiting q nodes of partition i to neighboring partition such that following condition holds good

$$Q(\mathcal{A}(\mathcal{G}_i))|_{n_i-q} \leq [Q(\mathcal{A}(\mathcal{G}_i))|_{n_i}]$$

2. Create a partition in \mathcal{G}_i on n_i nodes such that $[Q(\mathcal{A}(\mathcal{G}_i))]^{n_i} \geq Q(\mathcal{A}(\mathcal{G}_i))|_{n_i-p}$ and $[Q(\mathcal{A}(\mathcal{G}_i^p))] > Q(\mathcal{A}(\mathcal{G}_i))|_p$ (For example: This means p nodes in n_i partition are put on to power line network.)

Each node now can be represented in the \mathcal{G}^2 graph with k nodes as $\mathcal{P}_1, \dots, \mathcal{P}_k$ and corresponding \mathcal{A} matrix as $\mathcal{A}(\mathcal{G}_i)$ and dynamic load as $Q(\mathcal{A}(\mathcal{G}_i))$ where $i = 1 \dots k$. This means that $Q(\mathcal{A}(\mathcal{G}_i)) = Q(\mathcal{P}_i)$. Now each \mathcal{P}_i can either be underloaded or overloaded as mentioned before. So \mathcal{P}_i belongs to either U_i or L_i hence in graph $\mathcal{G}^2(Q(\mathcal{P}_i)) \in \{U_i, L_i\}$, $\forall i = \{1 \dots k\}$

Theorem 1: Consider Graph $\mathcal{G}^2 (\{U, L\}, e)$, where U is set of vertices operating at overloaded condition and L is set of nodes operating at under load condition with $\mathcal{P}_1, \dots, \mathcal{P}_k$ nodes and also consider that for $\mathcal{P}_1, \dots, \mathcal{P}_k$, theorem 2 is satisfied; then with k partitions the load can be balanced by approach of node transition iff their bipartite graph with U and L exist for \mathcal{G}^2 .

Proof: If overloaded nodes (partitions) can transit nodes inside the partition such that the under loaded partition will have more to accommodate as compared to the loaded partition hence such condition becomes the necessary condition. The proof of sufficiency follows from the contradiction. Consider that $\mathcal{G}^2 (\{U, L\}, e)$ is not bipartite then it means that one of the overloaded partition nodes \mathcal{P}_i is in neighborhood of another overloaded partition \mathcal{P}_j . Thus the transition of nodes from one overloaded partition (node) to the other overloaded partition can be expected. Hence **Bipartite graph formation between U_i and L_i nodes is necessary and sufficient condition for node transitions in \mathcal{G}^2 graph.**

5.1 Constraints to Be Followed for Node Transitions from One Partition to Another

There are three major constraints which must be followed to enable movement of a node from one partition \mathcal{G}_i to another neighboring partition \mathcal{G}_j

1. $R_i \geq U_i$
2. $R_j \leq L_j$
3. $\mathcal{G}^2 (\{U, L\}, e)$ must be bipartite between U and L

Observation I: A node p transiting from one partition i to another partition j needs only relabeling within the global adjacency matrix \mathcal{A} such that row and column corresponding to node p in \mathcal{A} moves from $\mathcal{A}(\mathcal{G}_i)$ to $\mathcal{A}(\mathcal{G}_j)$. Since this transition is only affecting the active/passive table entries of partitions i and j gateways the whole network need not be defunct, only partitions i and j can stop their operations. There is also no need to re-compute the partitions as in earlier cases.

6 Step III: Mathematical Constraints for Node Transition to Powerline

In this step we prove that introducing a powerline network to a node within the WMN is analogous to the partitioning procedure performed recursively. The introduction of the powerline network to any node within the WMN is defined as two part process. First we define constraints on identifying the node which can be moved to powerline. Secondly we explain how the node remains connected to the WMN and preserves its integrity even when it is on another wired network. This will ensure that the node can be recalled to exactly the same location within same neighborhood in spite of its association to a wired network which in this case is BPL.